



Attorney Docket No.: 60188-084

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of

Shigeki FURUYA, et al.

Serial No.: 09/922,671

Filed: August 07, 2001

For: CMOS BASIC CELL AND METHOD FOR FABRICATING SEMICONDUCTOR  
INTEGRATED CIRCUIT USING THE SAME

Group Art Unit: 2815

Examiner: Matthew E. WARREN

RECEIVED  
JUL -3 2002  
PATENT  
TECHNOLOGY CENTER  
6/20/02  
J. Steptun  
7-12-02

**AMENDMENT UNDER 37 C.F.R. § 1.111**

Hon. Assistant Commissioner for Patents  
Washington, DC 20231

Sir:

In response to the Office Action mailed April 1, 2002, having a shortened  
statutory period for response set to expire on July 1, 2002, please amend the above-  
identified application as follows:

**IN THE DRAWINGS:**

Please amend Figs. 30-41 as illustrated on the enclosed marked-up copies  
thereof. As shown, Figs. 30-41 have been amended to include the legend "Prior Art".